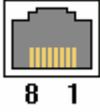
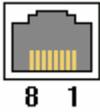


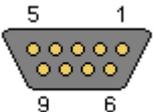
Ethernet port, standard NIC pin layout

RJ45	Pin	Signal	Comment
	1	TX+	
	2	TX-	
	3	RX+	
	4	GND 1	75 Ohm
	5	GND 1	
	6	RX-	
	7	GND 2	75 Ohm
	8	GND 2	

CAN-bus ports

RJ45	Pin	Signal	Comment
	1	CAN-H	
	2	CAN-L	
	3	GND	
	4	n.c.	
	5	n.c.	
	6	reserved	
	7	GND	
	8	n.c.	

Interlock connector

DSUB9 female	Pin	Signal	Comment
	1	CRATE_ENABLE	TTL input (1 kOhm resistor to GND) A high level allows the channels to be switched on by software A low level forces all channels to switch off with their specified down ramp.
	2	CRATE_FAST_OFF	TTL input (1 kOhm resistor to GND) A high level forces all channels to switch off as fast as possible. Any ramp-down settings are ignored.
	3	reserved	
	4	reserved	
	5	GND	Ground reference
	6	CRATE_STATUS	TTL output (1 kOhm resistor to GND, 100 Ohm resistor to protect the output buffer) This signal is driven high, if one or more channels of the MPOD system do have a non-zero output voltage
	7	reserved	
	8	reserved	
	9	reserved	